

*FIG.1*

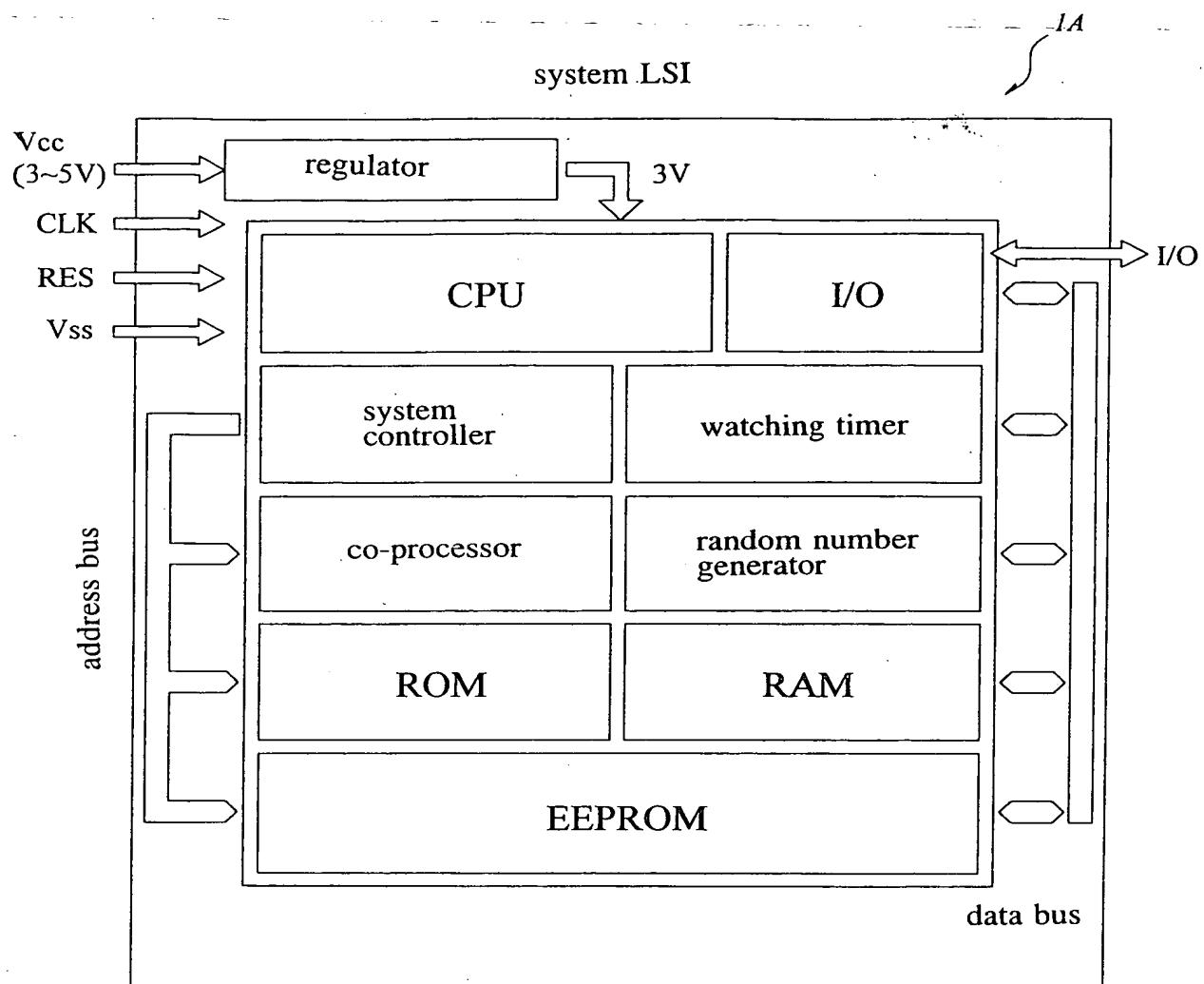
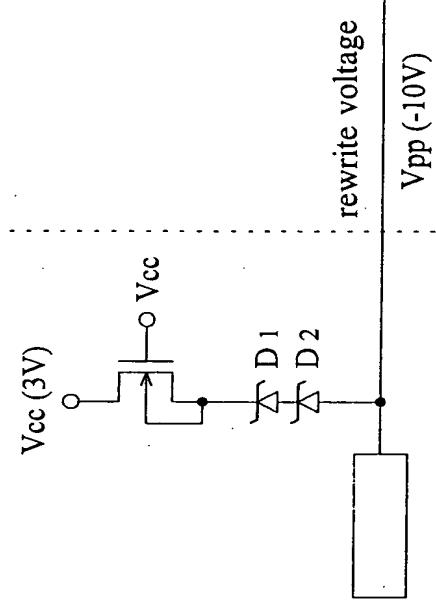


FIG.2

constant voltage generating circuit



EEPROM

memory mat

rewrite voltage  
 $V_{pp} (-10V)$

boosting circuit

FIG. 3

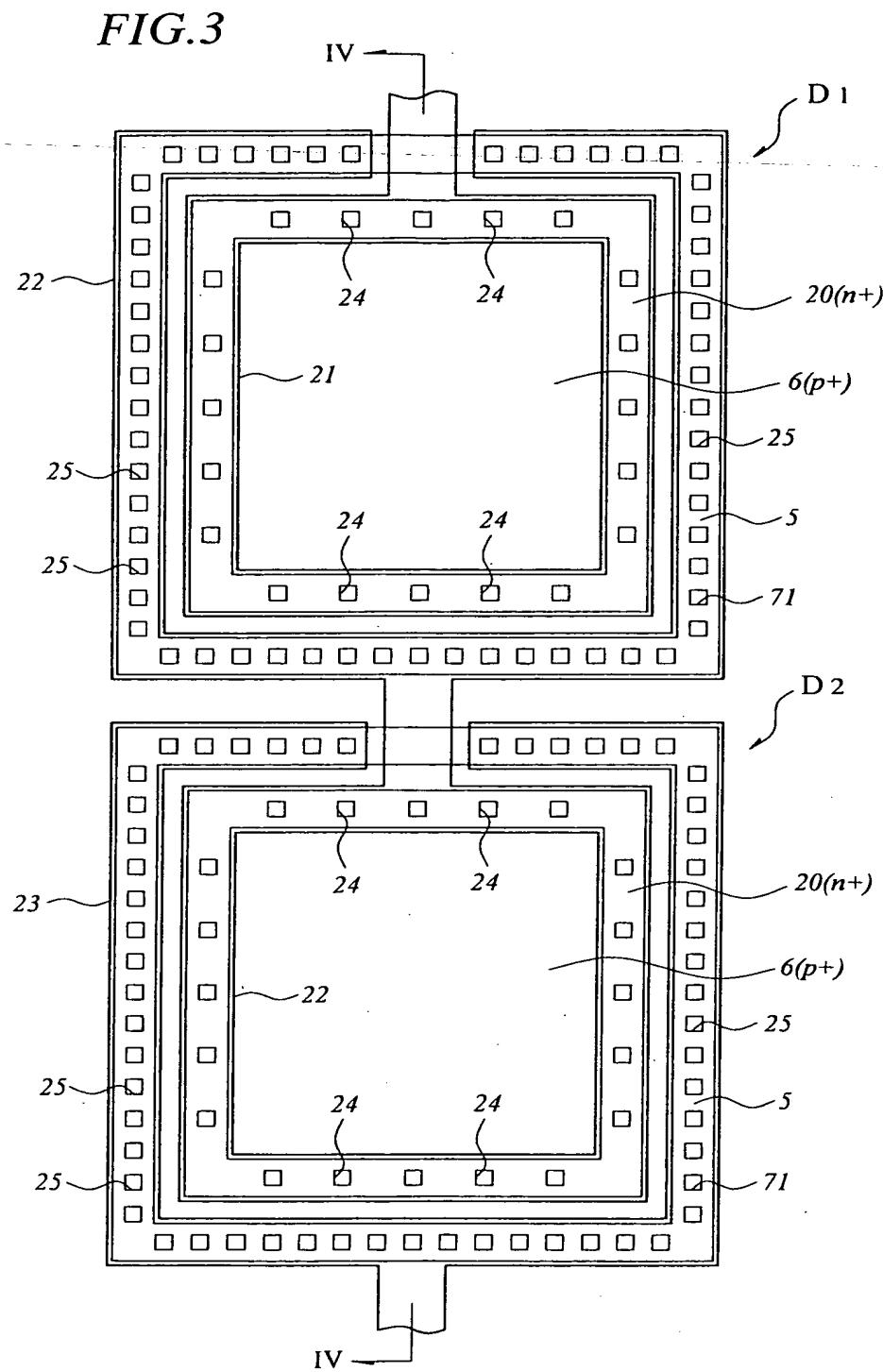
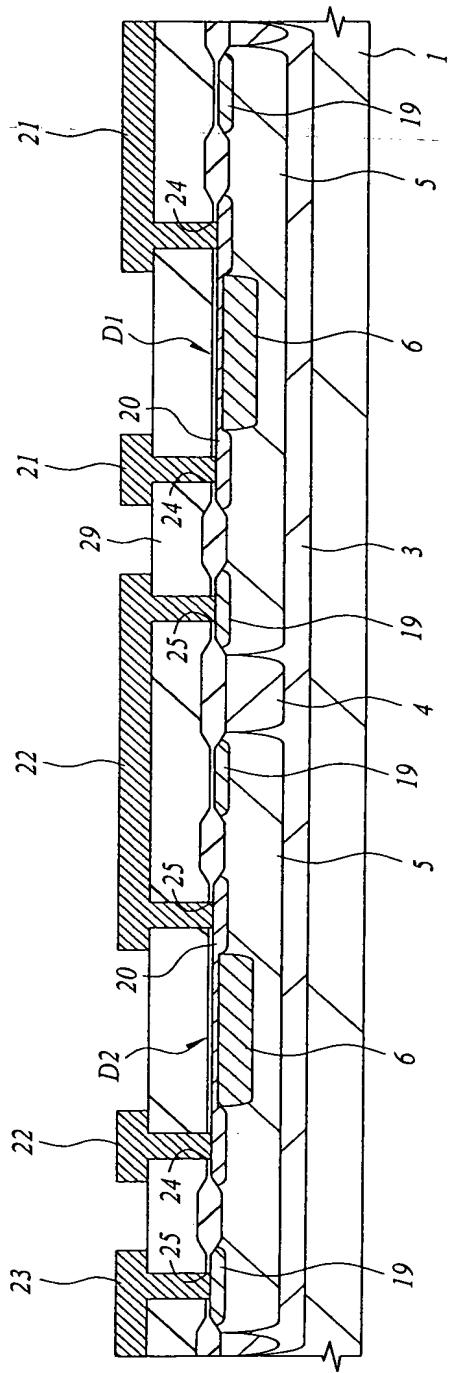
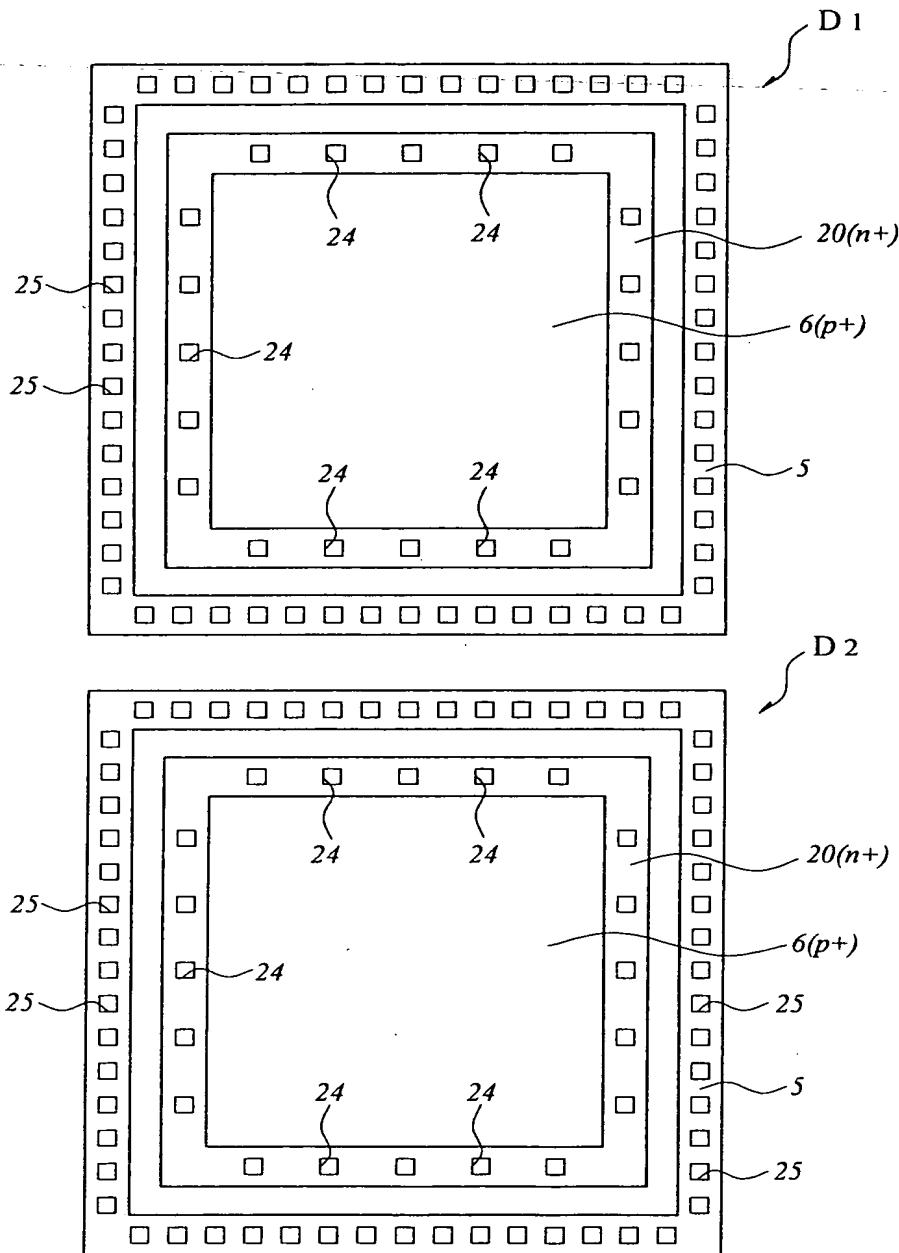


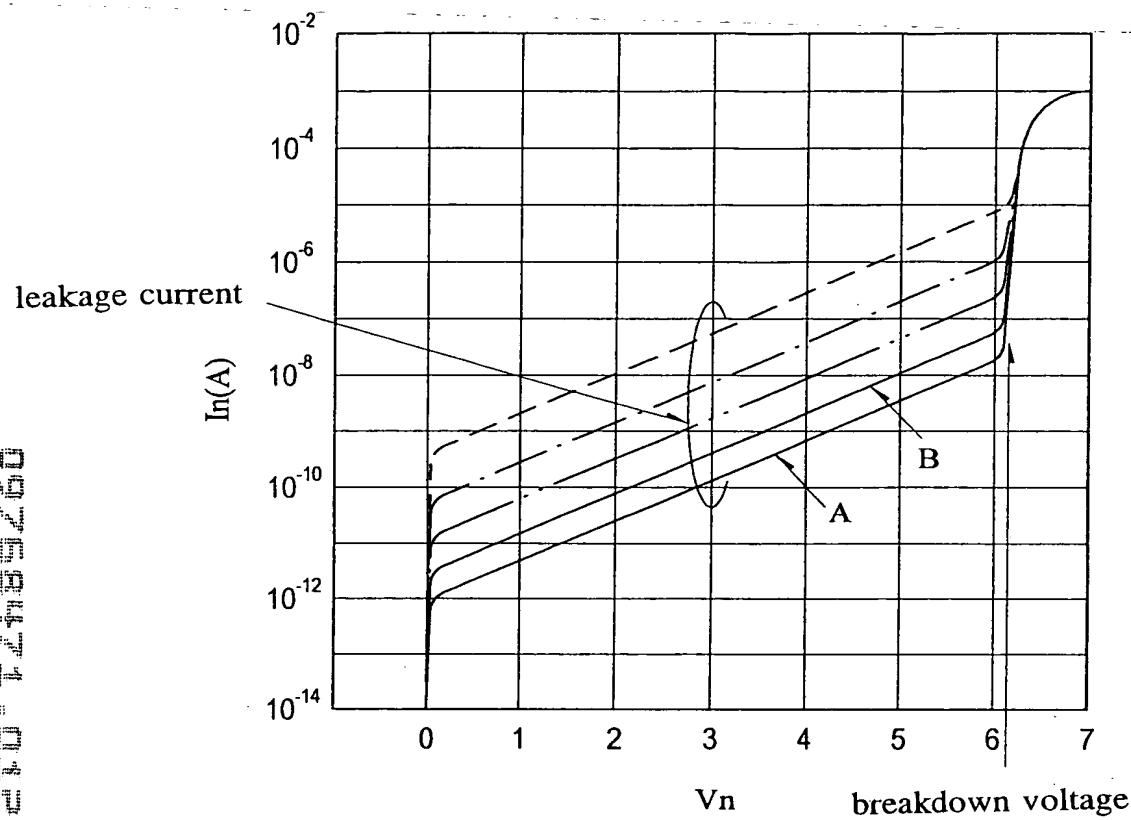
FIG.4



*FIG.5*

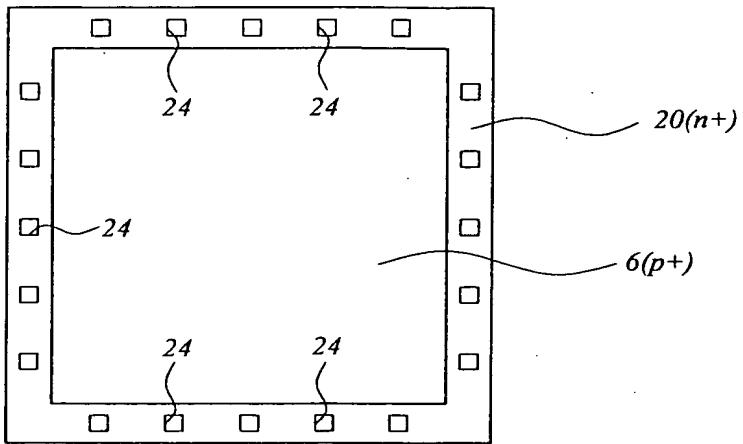


*FIG.6*

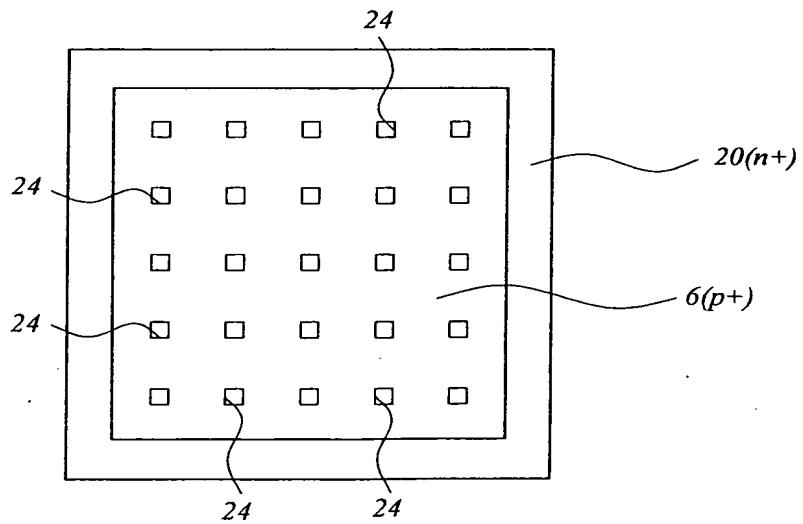


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*FIG.7*

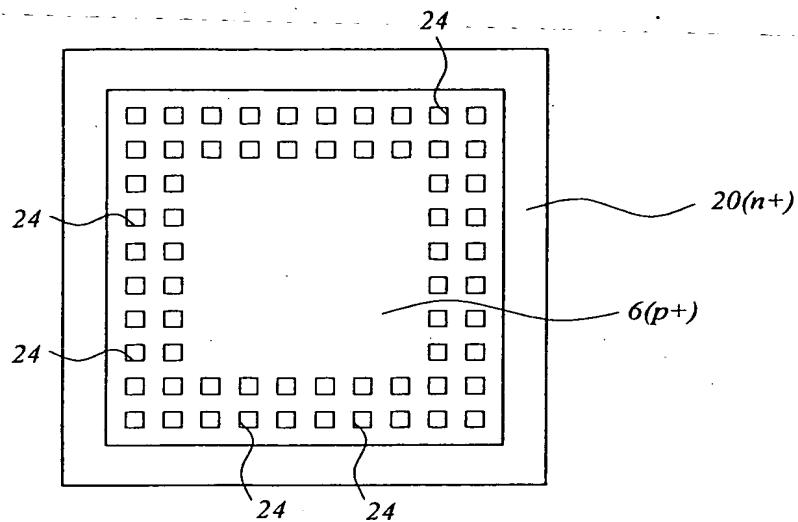


*FIG.8*

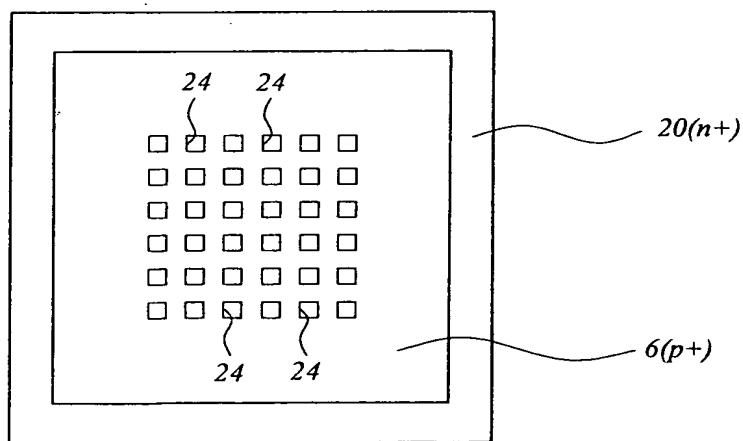


20240 = F2489460

*FIG.9*



*FIG.10*



*FIG.11*

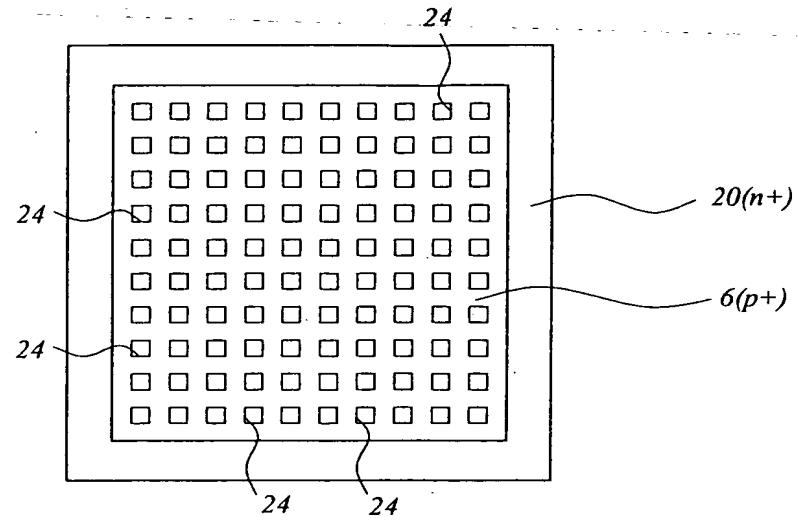


FIG.12

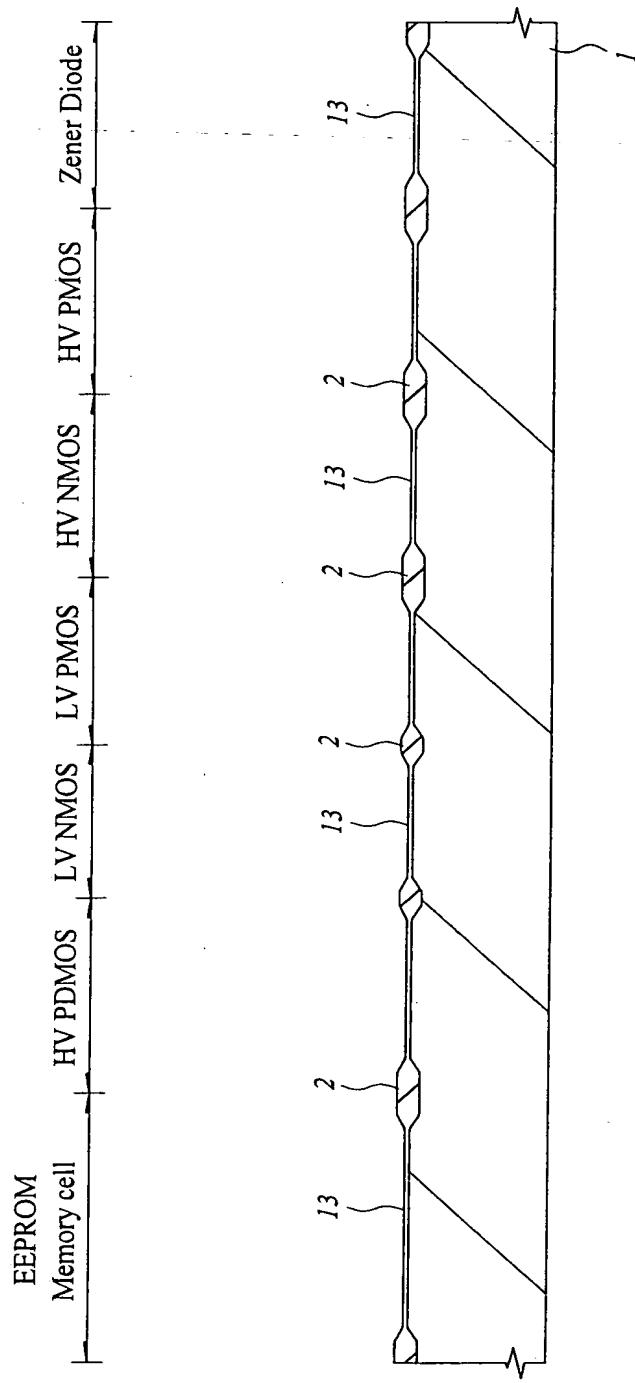


FIG.13

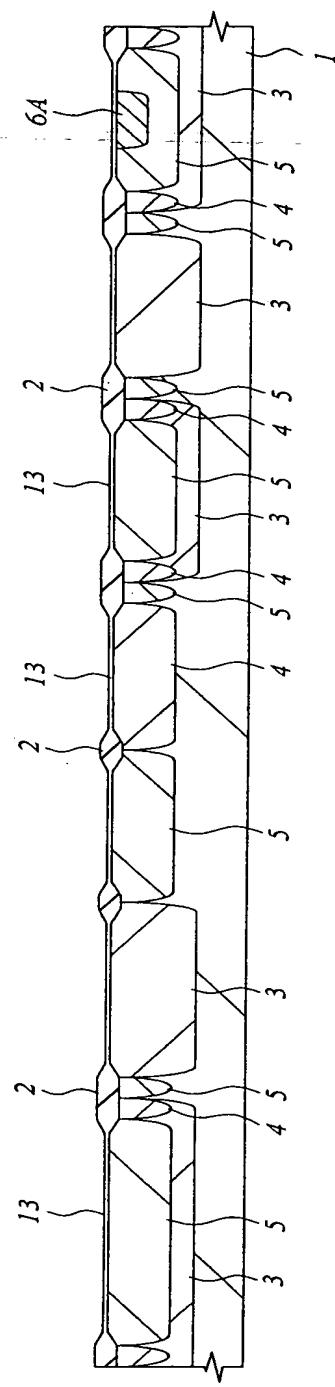


FIG.14

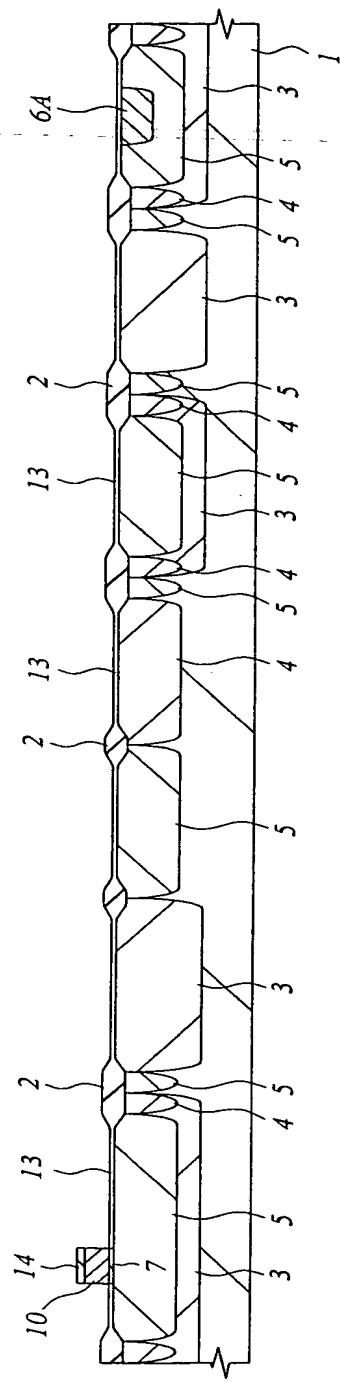


FIG.15

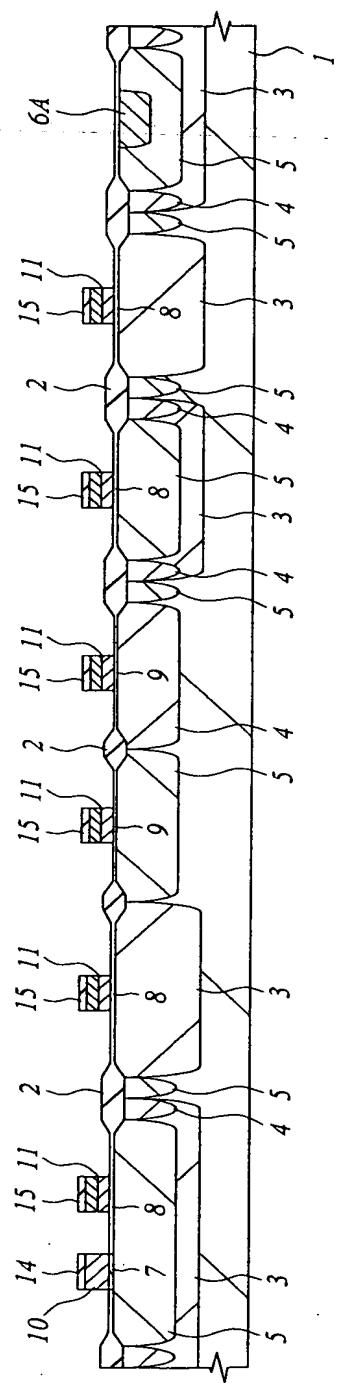


FIG. 16

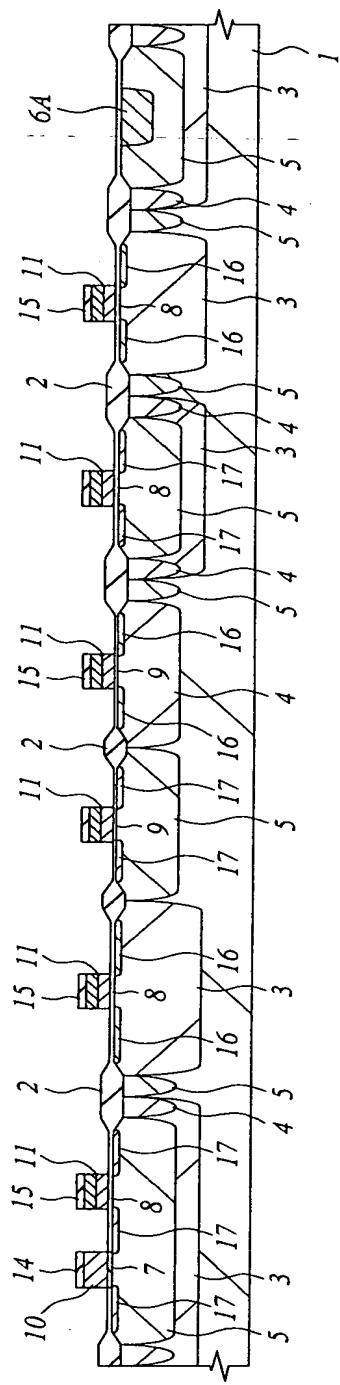


FIG. 17

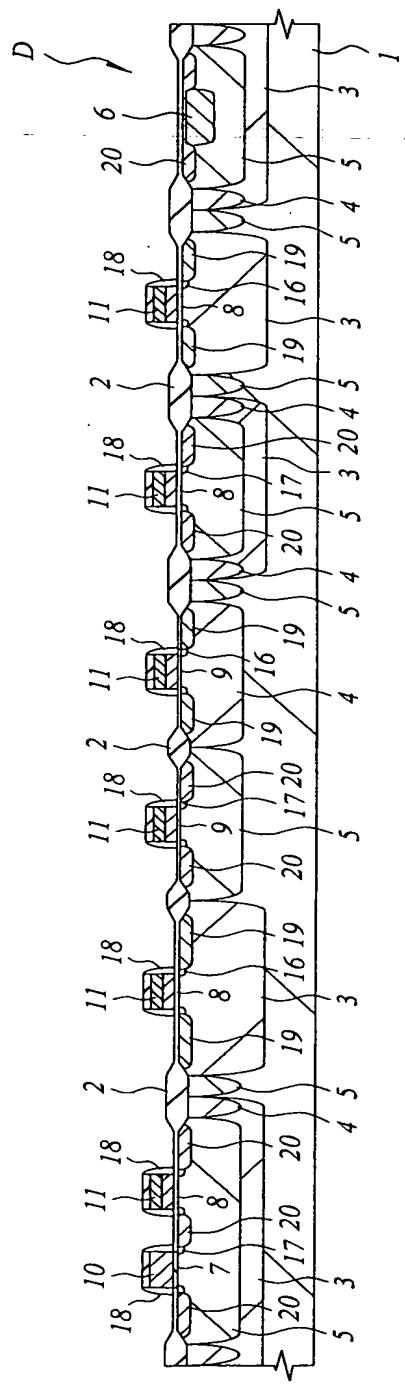


FIG. 18

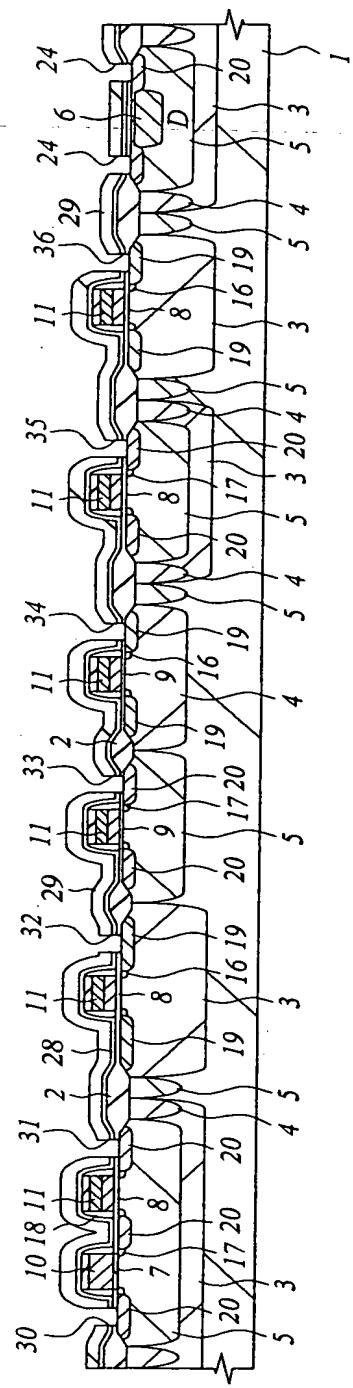


FIG. 19

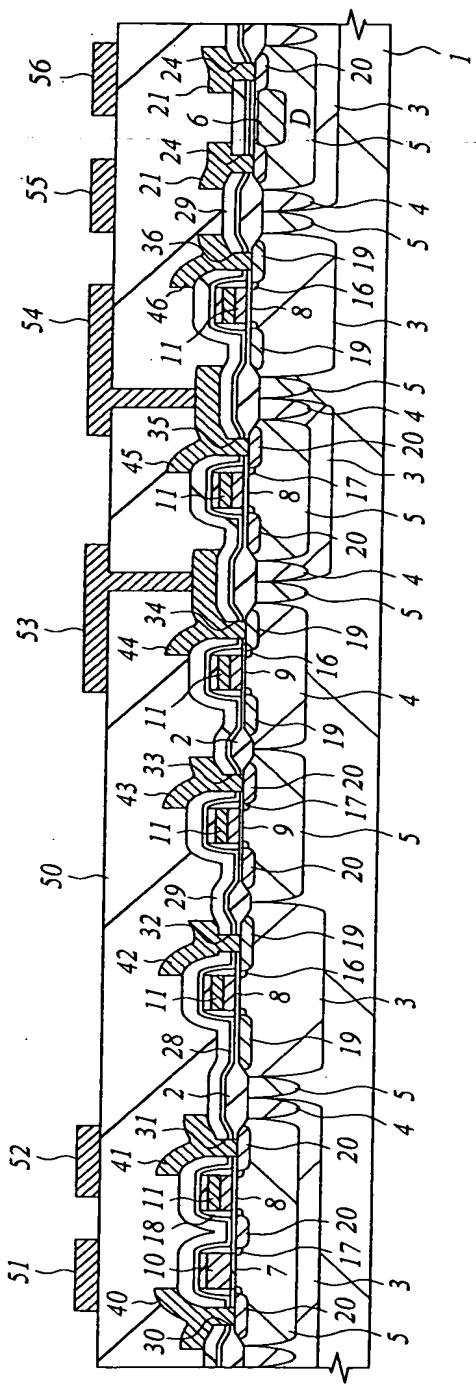
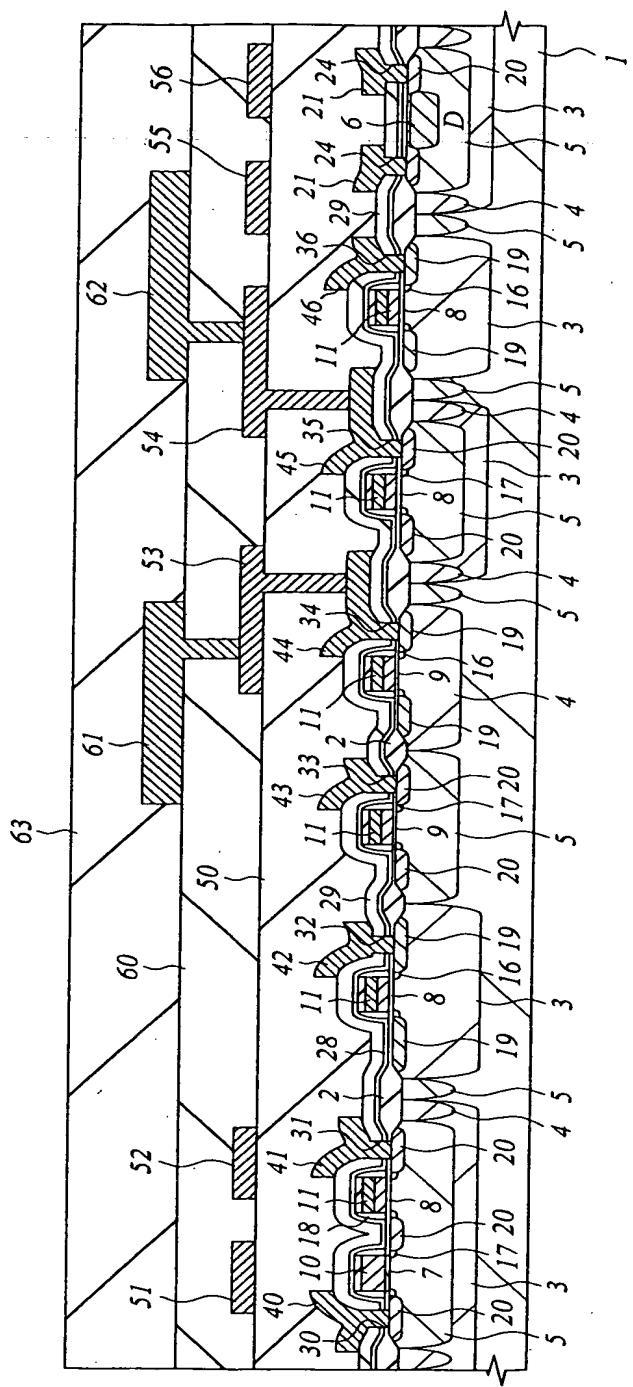
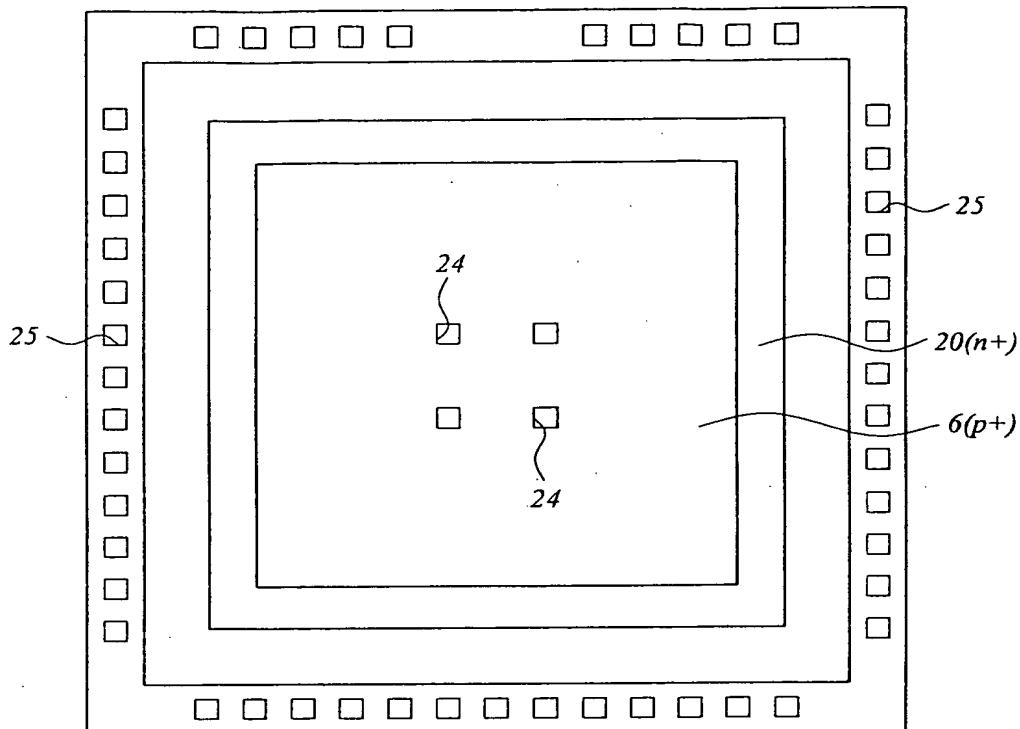


FIG.20

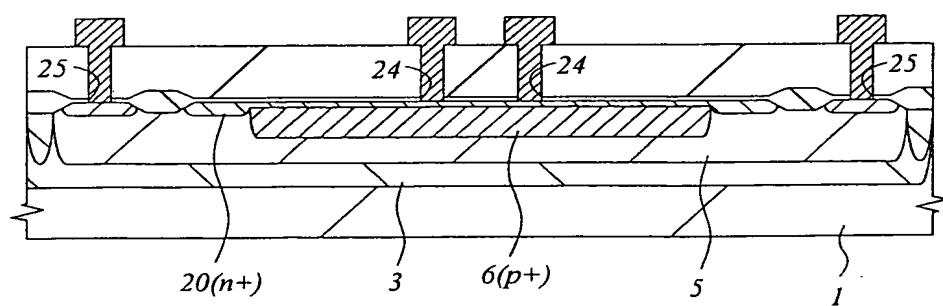
FIG.20



*FIG.21(a)*

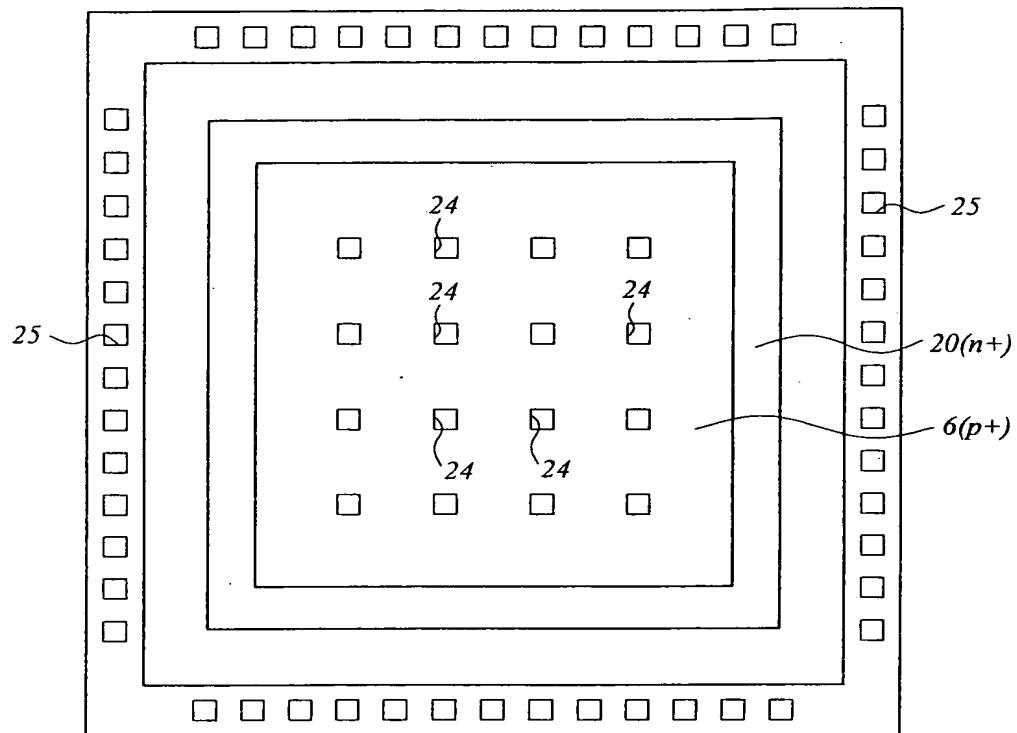


*FIG.21(b)*

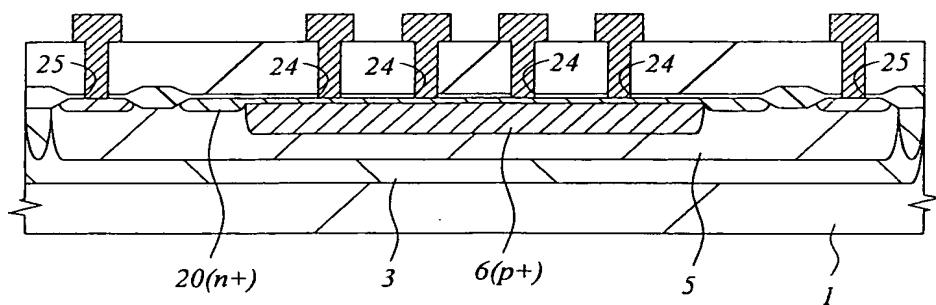


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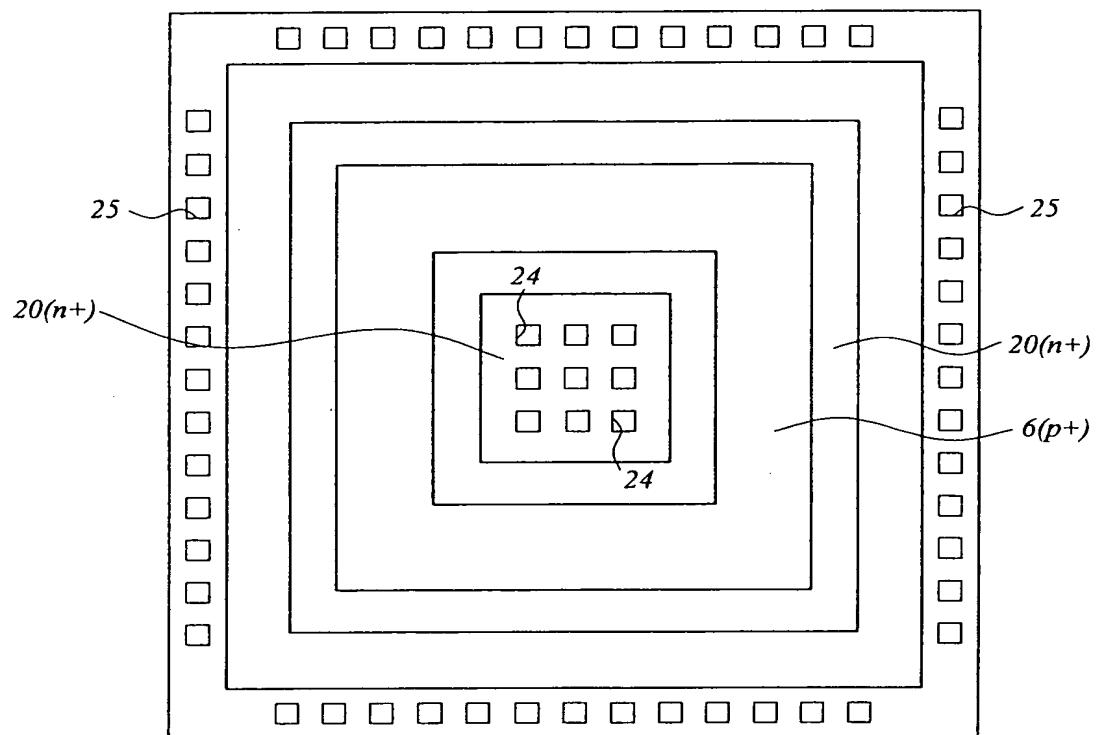
*FIG.22(a)*



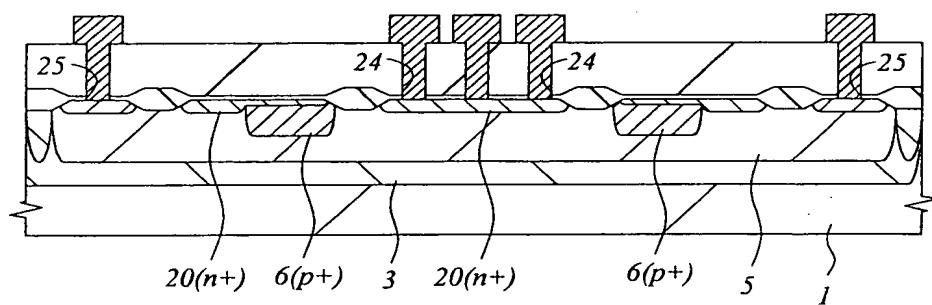
*FIG.22(b)*



*FIG.23(a)*

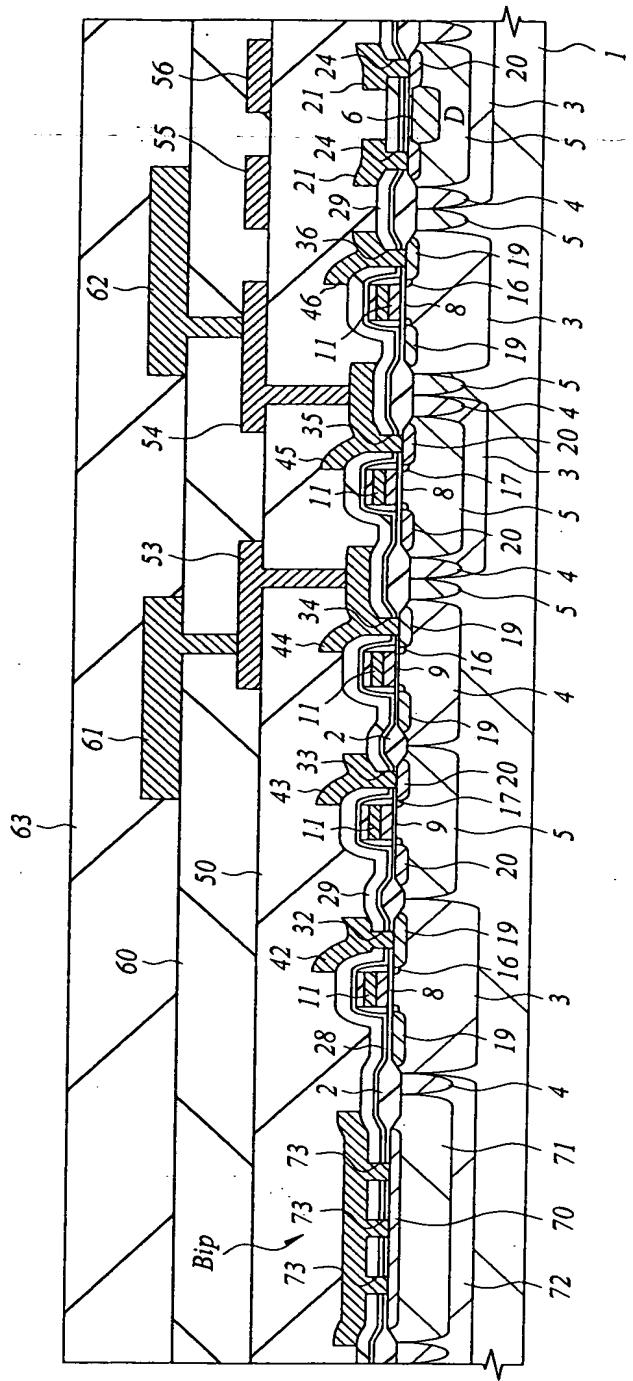


*FIG.23(b)*

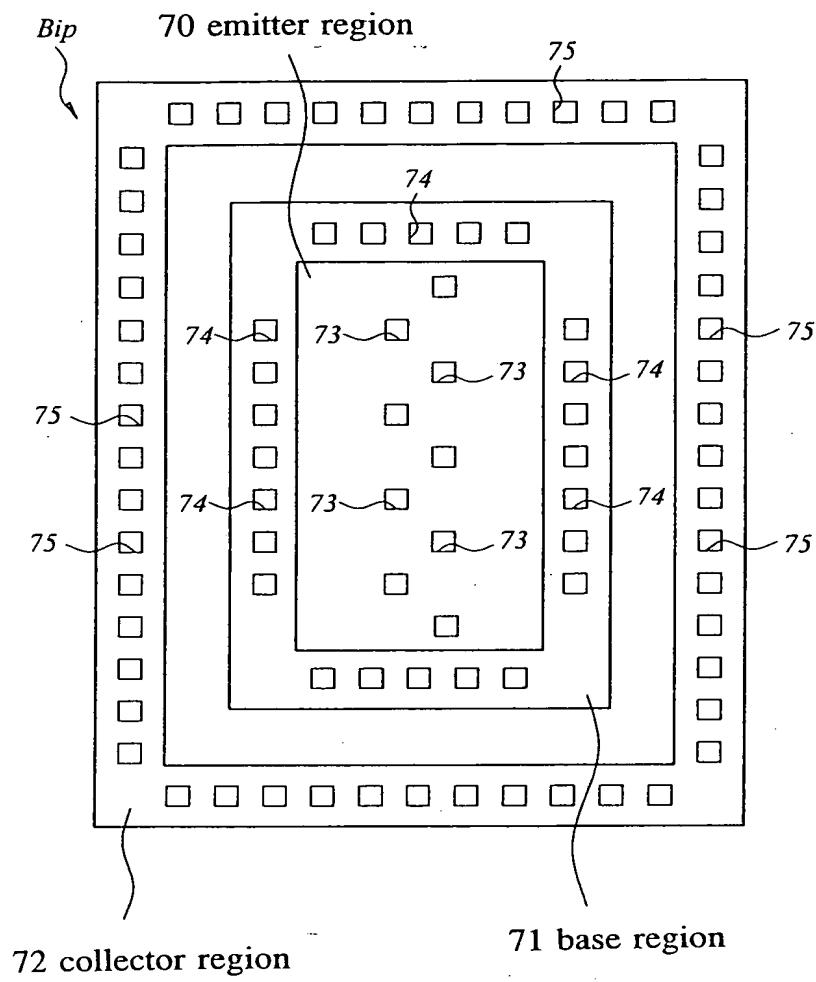


092638474 = 0942500

FIG. 24



*FIG.25*



TO 92 T0 - T24 1859 X60

*FIG.26*

